

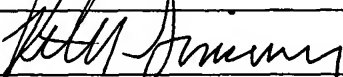
DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION

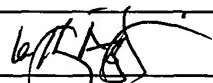
As a below-named inventor, I hereby declare that:

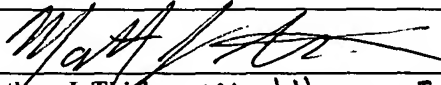


- My residence, post-office addresses and citizenship are as stated below next to my name.
- I believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought by the application for U.S. Letters Patent entitled **MECHANISM FOR INTEGRATING PROGRAMMABLE DEVICES INTO SOFTWARE BASED FRAMEWORKS FOR DISTRIBUTED COMPUTING**, the specification of which is attached hereto and identified by Docket No. **D-4669 of BAE SYSTEMS Information and Electronic Systems Integration Inc.**
- I hereby state that I have reviewed and understand the contents of the above-identified application specification, including the claims.
- I acknowledge the duty to disclose information that is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, paragraph 1.56(a).
- I hereby claim foreign priority benefits under Title 35, United States Code 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed: NONE
- I hereby claim the benefit under 35 U.S.C. 119(e) of any United States provisional application(s) listed below: U.S. Provisional Application No. 60/407,554, filed August 29, 2002
- I hereby claim the benefit under 35 U.S.C. 120 of any United States application(s), or 365(c) of any PCT international application designating the United States of America, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of 35 U.S.C. 112, I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application: U.S. Application No. 10/303,441, filed November 25, 2002, and U.S. Application No. 10/233,338 filed August 30, 2002
- I hereby declare that all statements made herein of my knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.
- **Power of Attorney:** We hereby appoint the following persons as our attorney, with full power of substitution, delegation and revocation, to prosecute this application, to make alterations and amendments therein, to receive the patent and to transact all business in the Patent and Trademark Office connected therewith: **Scott J. Asmus, Reg. No. 42,269; Neil F. Maloney, Reg. No. 42,833; Daniel J. Long, Reg. No. 29,404; and Andrew P. Cernota, Reg. No. 52,711**
- Please direct all telephone calls to **Scott J. Asmus or Neil F. Maloney at (603) 886-6100.**
- Please address all correspondence to **Maine and Asmus, PO Box 3445, Nashua, NH 03061-3445.**
- All Inventor's are listed below:
 - Peter Simonson
 - Kazem Haji-Aghajani
 - Matthew J. Thiele
 - Frank D. Stroili
 - Kevin P. Natwick
 - Robert P. Boland

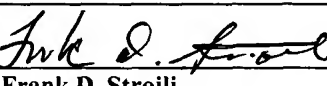
DECLARATION SIGNATURE PAGE(S) ATTACHED

**MECHANISM FOR INTEGRATING PROGRAMMABLE DEVICES INTO SOFTWARE BASED
FRAMEWORKS FOR DISTRIBUTED COMPUTING**
SIGNATURE PAGE 1 of 2

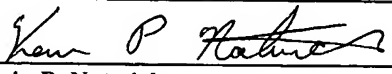
Signature of Inventor:	
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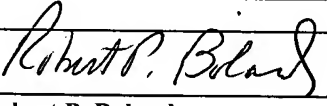
Signature of Inventor:	
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P. O. Address:	Same as Residence

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**MECHANISM FOR INTEGRATING PROGRAMMABLE DEVICES INTO SOFTWARE BASED
FRAMEWORKS FOR DISTRIBUTED COMPUTING**
SIGNATURE PAGE 2 of 2

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